

IN THE CLAIMS

Please amend the claims as follows, substituting any amended claim(s) for the corresponding pending claim(s):

- 1 1. (Original) A method for manufacturing an on-chip inductor comprises:  
2 creating a dielectric layer; and  
3 creating a conductive winding on the dielectric layer, wherein the conductive winding has a  
4 substantially square geometry, wherein corners of the conductive winding are geometrically shaped to  
5 reduce impedance of the on-chip inductor at an operating frequency.
- 1 2. (Original) The method of claim 1, wherein the creating of the conductive winding further  
2 comprises: creating the geometric shaping of the corners to include an interior angle per corner of  
3 approximately ninety degrees, and an exterior angle per corner of approximately one hundred thirty-five  
4 degrees.
- 1 3. (Original) The method of claim 1, wherein the creating of the conductive winding further  
2 comprises: creating the geometric shaping of the corners to include an interior angle per corner of  
3 approximately one hundred thirty-five degrees, and an exterior angle per corner of approximately one  
4 hundred thirty-five degrees.
- 1 4. (Original) The method of claim 1 further comprises: creating the conductive winding to have a  
2 spiral configuration, wherein the corners of the spiral configuration are geometrically shaped to reduce  
3 impedance of the on-chip inductor at the operating frequency.
- 1 5. (Original) The method of claim 1, wherein the creating of the conductive winding further  
2 comprises: creating a first winding on a first layer; creating a second winding on a second layer; and  
3 connecting the first winding to the second winding with at least one bridge.
- 1 6. (Original) The method of claim 1, wherein the creating of the conductive winding further  
2 comprises: creating the geometric shaping of the corners to include angled exterior corners, wherein at  
3 least one angle per exterior corner reduces current turbulence in the corner at the operating frequency.
- 1 7. (Original) The on-chip inductor of claim 6, wherein the creating of the conductive winding  
2 further comprises: creating the geometric shaping of the corners to include angled interior corners,  
3 wherein at least one angle per interior corner further reduces current turbulence in the corner at the  
4 operating frequency.